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FIG. 1

10 -ADD R1, R2, R3 i SUB R4, R5, R6 i+1 12 Scalar LD R7 (R3) i+2 CODE i+3 BRA IF R4>0 FLOAD FR1 (R32) i+4 i+5 FLADD FR1, FR2, FR3 FLOATING FLMUL FR7, FR8, FR9 i+6 POINT

FIG. 2

SCALAR CODE

SCALAR CODE

SCALAR CODE

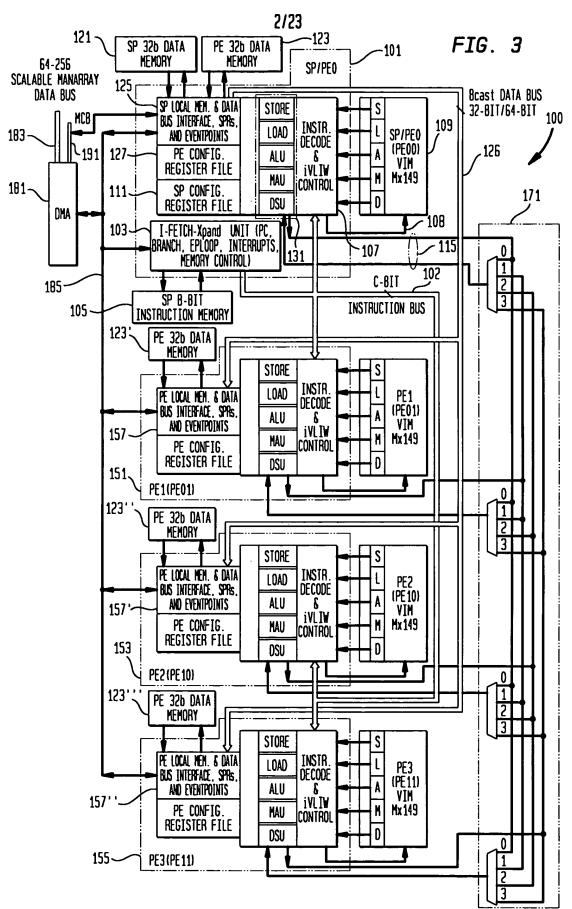
SCALAR CODE

FLOATING POINT CODE

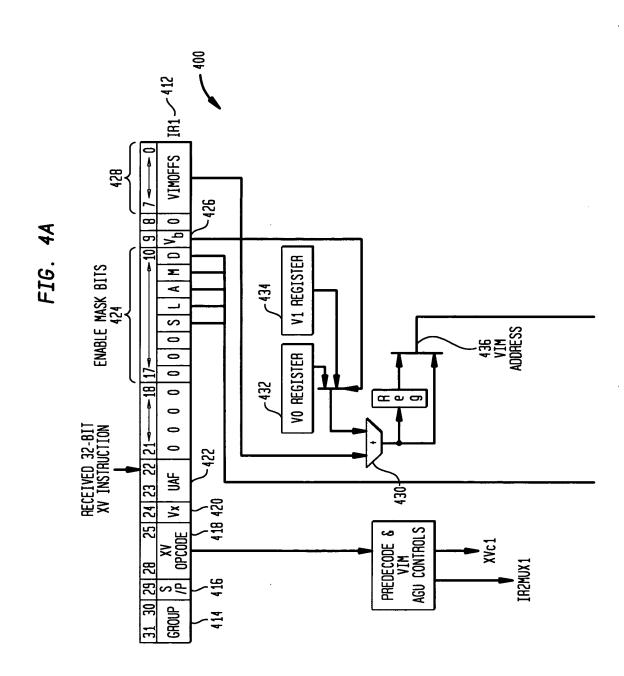
j Execute 1 26

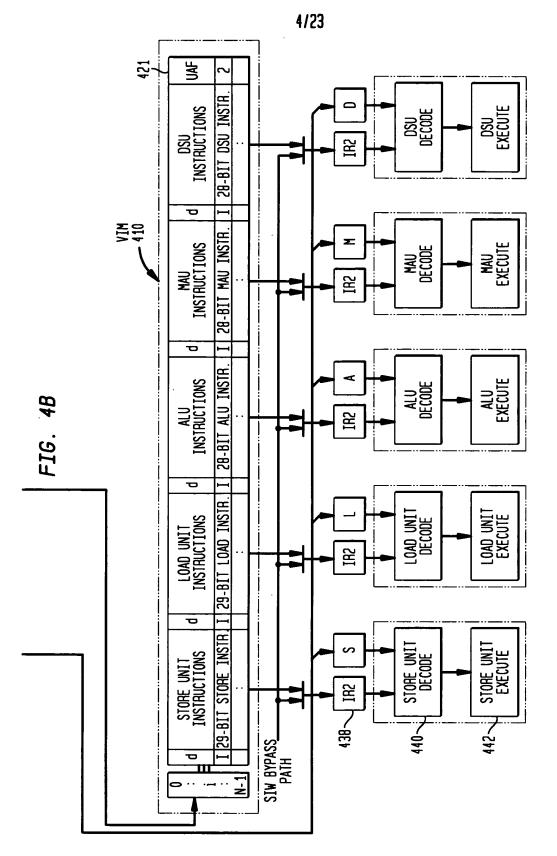
j+1 Execute 2 28

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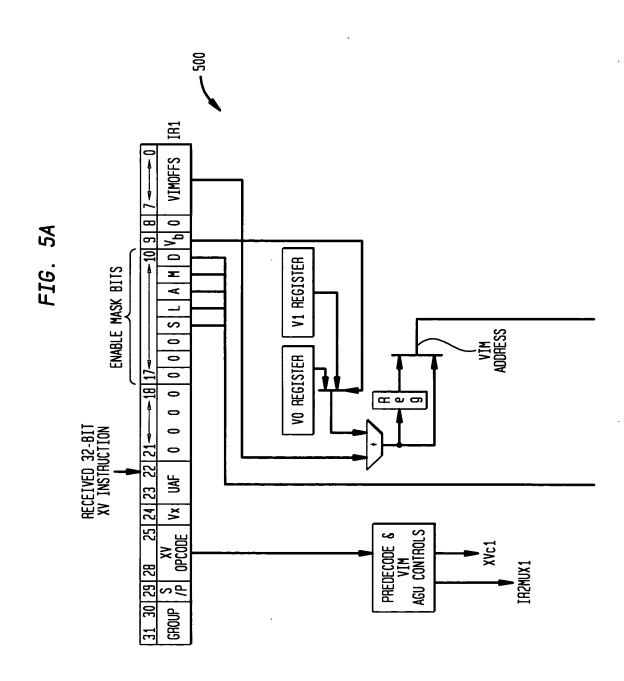


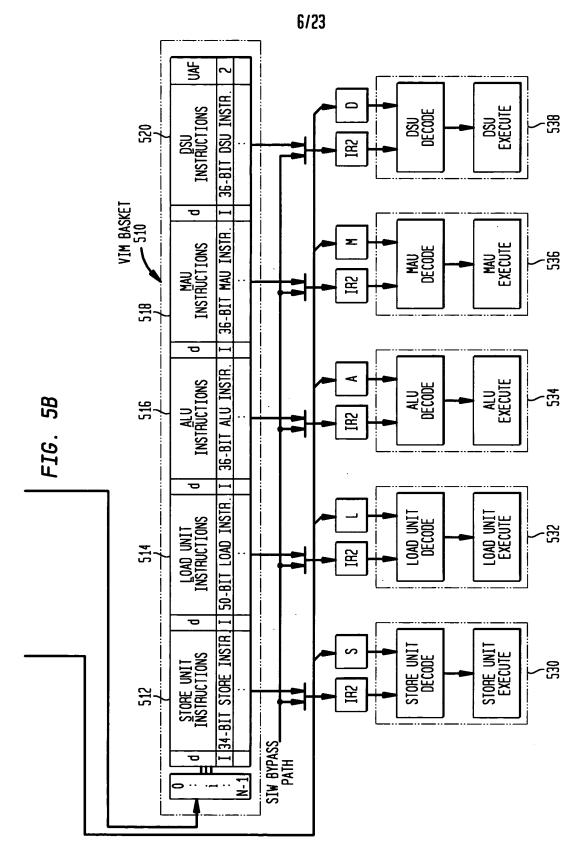
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SumpExt 8 31 30 29 28 27 26 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 Bye By 준 FIG. 6A (PRIOR ARI) Rte Rt MAVopcode 32-bit Encoding GROUP S/P UNIT

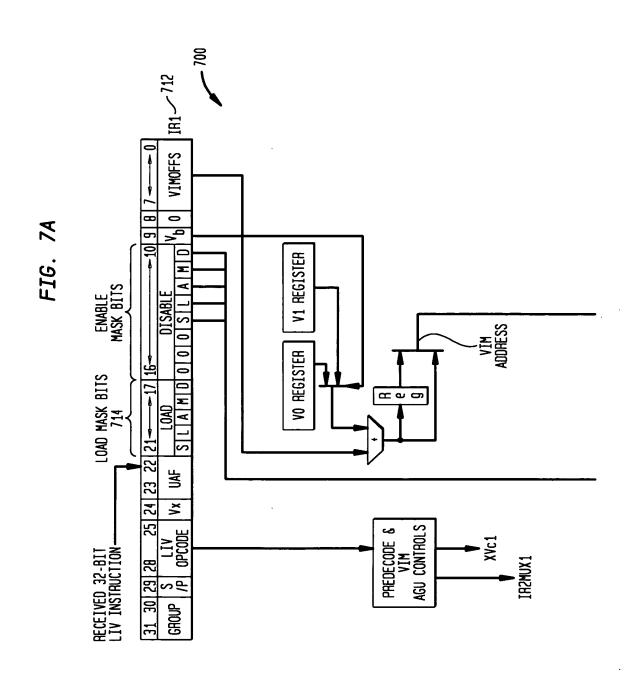
SumpExt' E3 -610 8 39|38|37|36|35|34|33|32|31|30|29|28|27|26|25|24|23|22|21|20|19|18|17|16|15|14|13|12|11|10| Rt' Rx' Ry' Ry' Ry' Ry' Ry' Ry' Rye' 0 Rye' . W. B. SLAMDunk 40-bit Encoding Example

FIG. 68

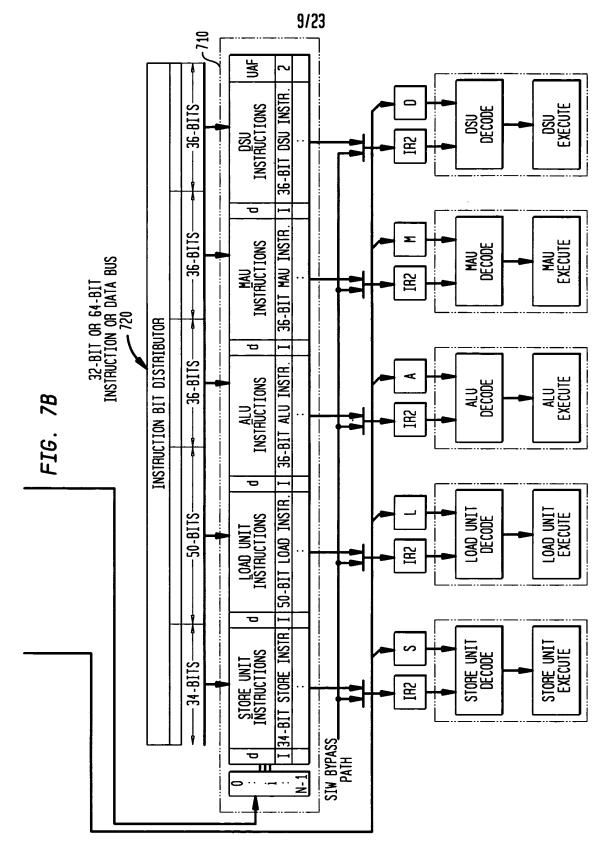
FIG. 6C

SumpExt <u>E3</u> 39|38|37|36|35|34|33|32|31|30|29|28|27|26|25|24|23|22|21|20|19|18|17|16|15|14|13|12|11|10|9|8|7|6| æ 620 Rye æ Rxe 32-bit Mapping to SLAMDunk 40-bit Encoding Example Rt Rte MAVopcode GROUP S/P UNIT

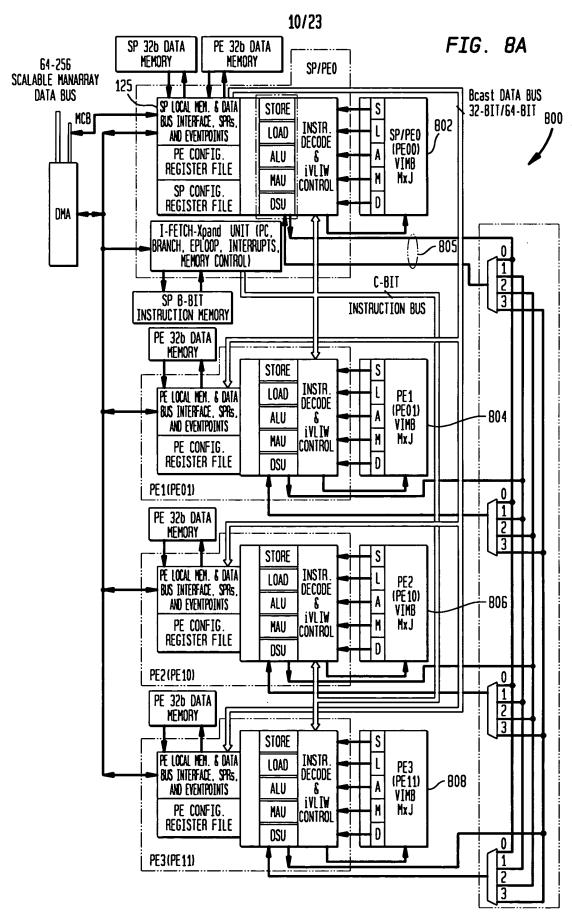
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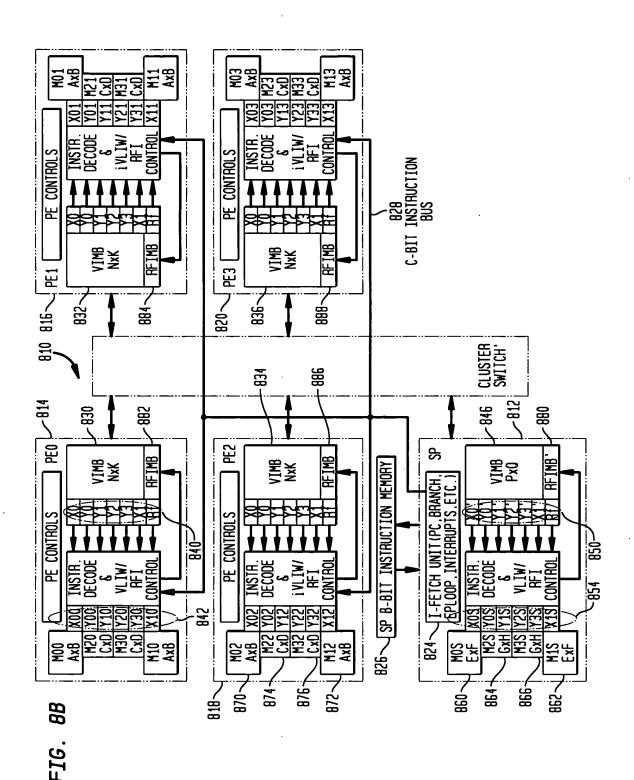
SERIAL NO.: 10/657,491 PETER H. PRIEST (919-806-1600)



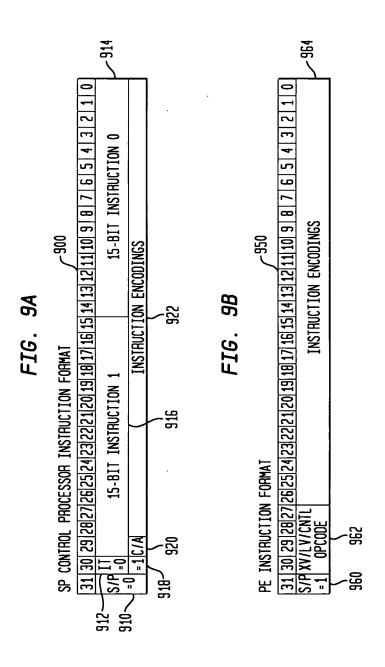
SERIAL NO.: 10/657,491 PETER H. PRIEST (919-806-1600)



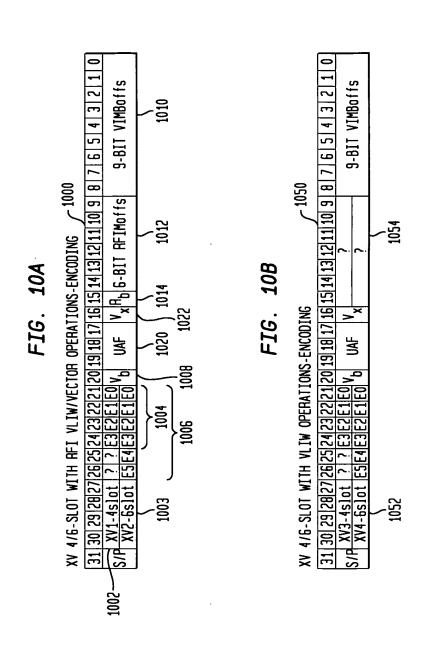
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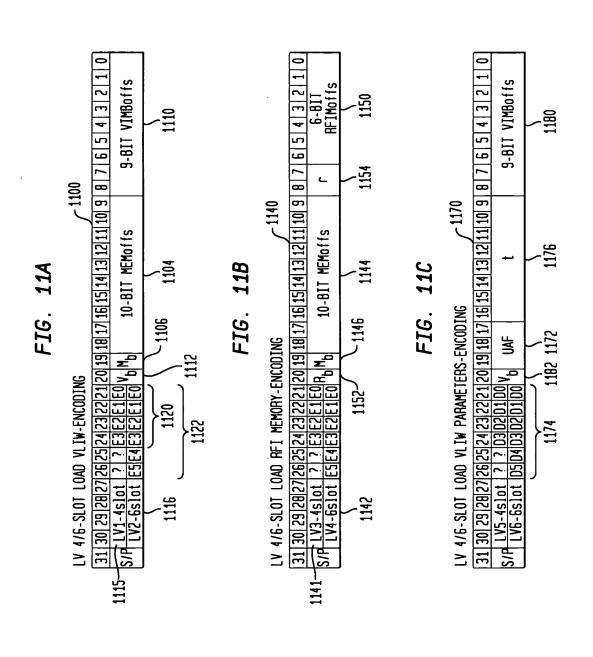
12/23



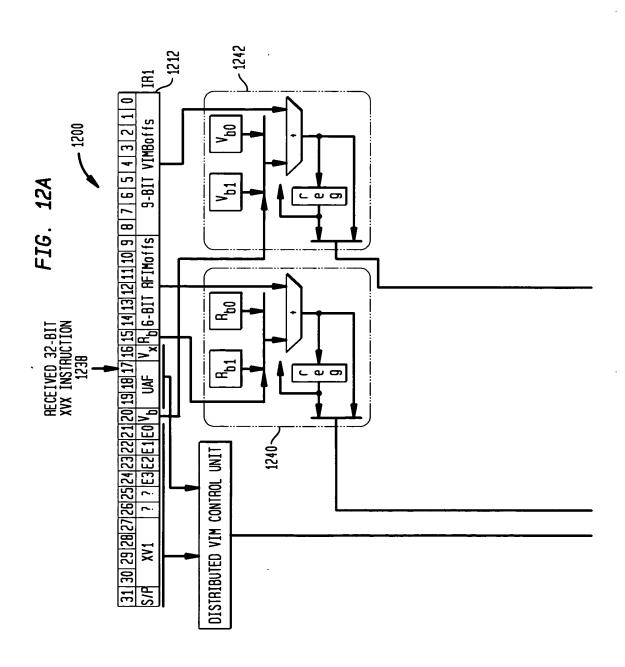
13/23



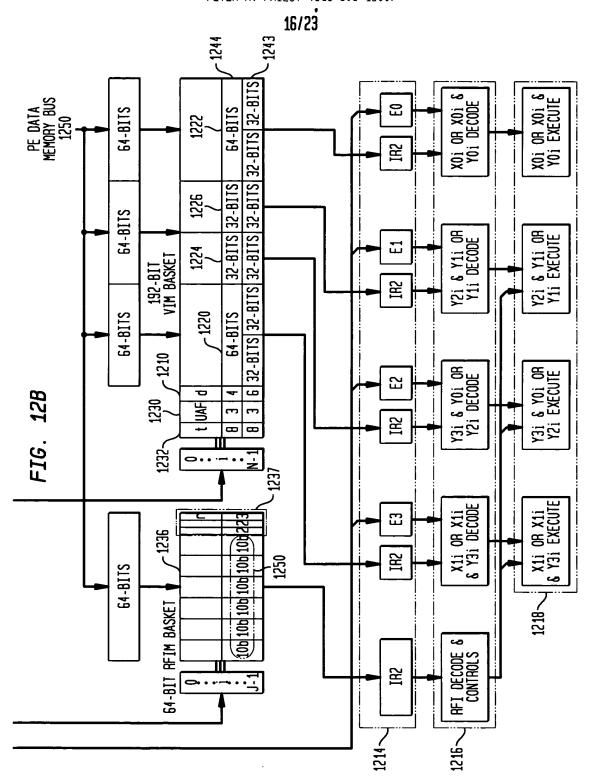
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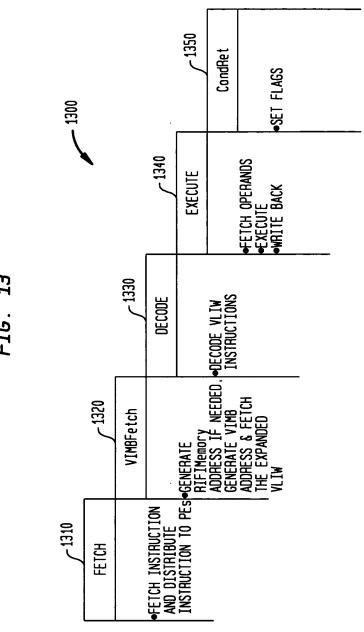
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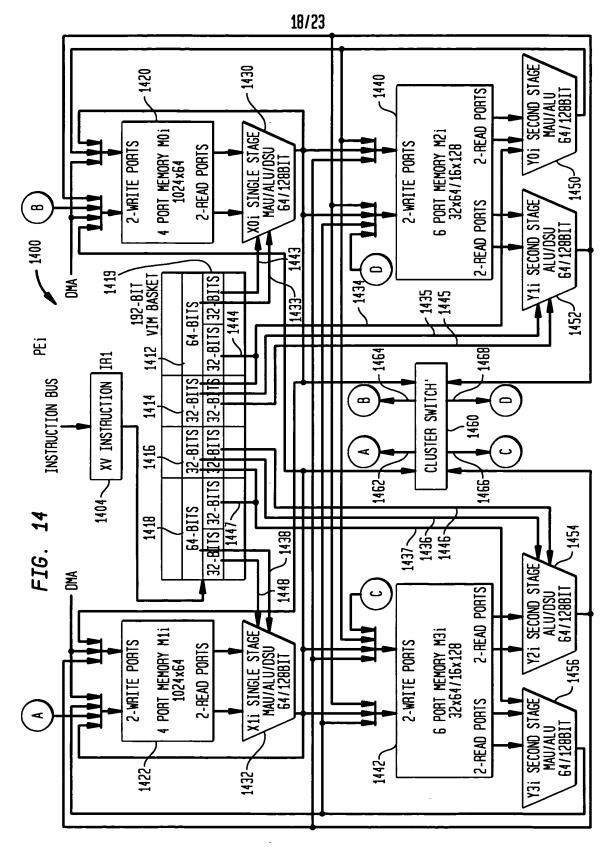


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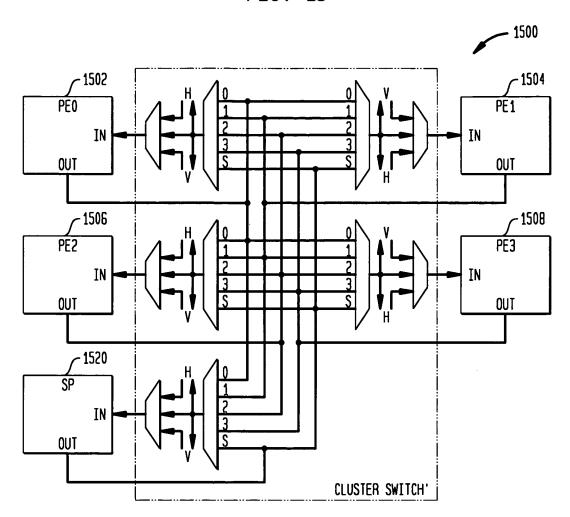




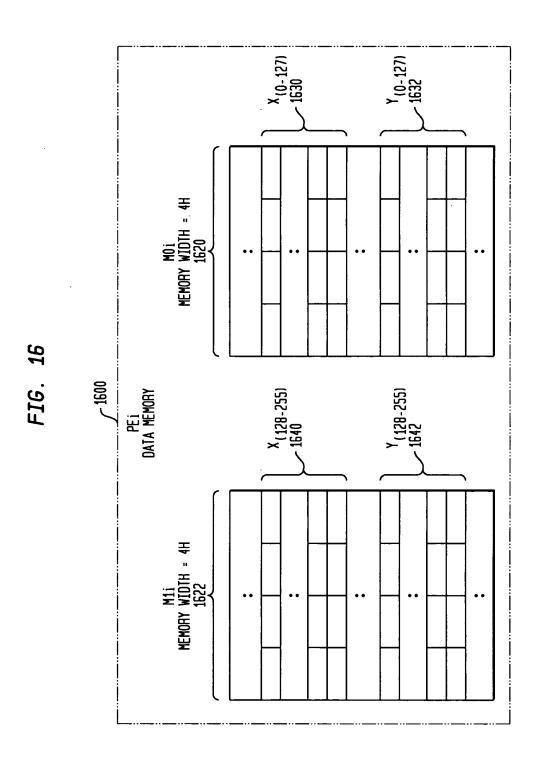
SERIAL NO.: 10/657,491 PETER H. PRIEST (919-806-1600)

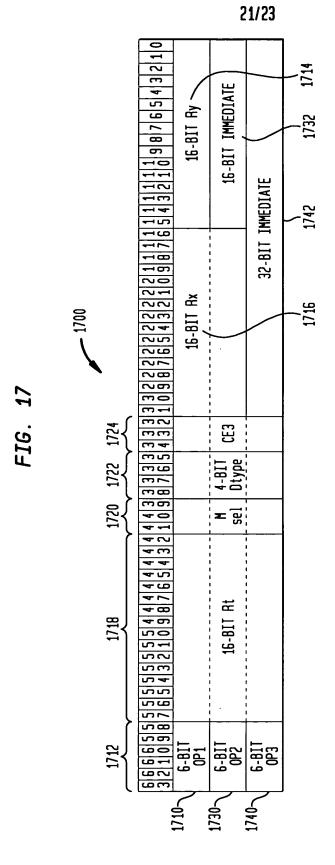
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FIG. 15



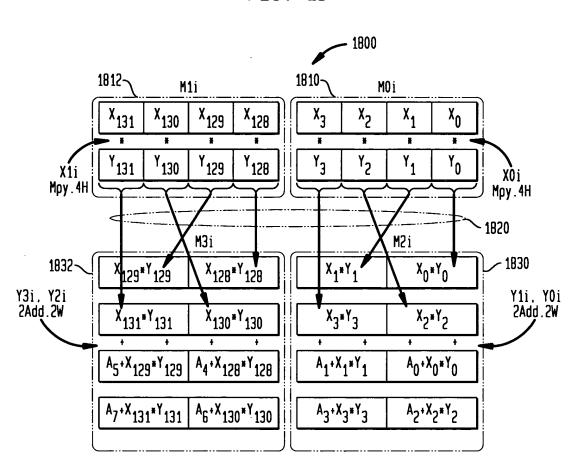
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FIG. 18



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FIG. 19

